

***Amendments to the Claims***

The listing of claims will replace all prior versions, and listings of claims in the application.

1-14. (Canceled)

15. (Currently Amended) A circuit, comprising:

a buffer;

a sampling circuit capable of storing a charge of a sample, the sampling circuit having a switch, an input, and an output; and

a damping circuit coupled between the buffer and the input of the sampling circuit;

wherein the damping circuit is adapted to reduce charge glitches when the switch closes;

wherein the sampling circuit is a switched-capacitor sampling circuit, wherein the switched-capacitor sampling circuit includes a capacitor having a first node and a second node, and wherein the first node is coupled to the switch and the second node is coupled to the output of the sampling circuit.

16. (Original) The circuit of claim 15, wherein the damping circuit comprises a low pass filter.

17. (Original) The circuit of claim 16, wherein the low pass filter is an RC low pass filter.

18. (Previously Presented) A circuit, comprising:
- a buffer;
  - a sampling circuit having a switch; and
  - a damping circuit coupled between the buffer and the sampling circuit;
- wherein the damping circuit is adapted to reduce charge glitches when the switch closes; and
- wherein the buffer includes
- a source-follower transistor adapted to generate an output signal from an input signal,
  - a replica transistor adapted to generate a replica signal from the input signal, and
  - a level shifting circuit that provides a level-shifted replica signal at a terminal of the source-follower transistor.
19. (Previously Presented) The circuit of claim 15, wherein the buffer includes a transistor having a source and a body, and wherein the source and the body are coupled to each other to reduce a signal dependent current.
20. (Previously Presented) The circuit of claim 15, wherein the buffer includes
- a transistor having a source; and
  - a current source coupled between the source and a voltage supply.
- 21-22. (Canceled)

23. (Previously Presented) The circuit of claim 18, wherein at least one of the source-follower transistor and the replica transistor has a source and a body, and wherein the source and the body are coupled to each other to reduce a signal dependent current.
24. (Previously Presented) The circuit of claim 18, wherein the buffer further includes a current source coupled between a source of the source-follower transistor and a voltage supply.
25. (Previously Presented) The circuit of claim 18, wherein the buffer further includes a current source coupled between a source of the replica transistor and a voltage supply.
26. (Previously Presented) The circuit of claim 18, wherein the sampling circuit is a switched-capacitor sampling circuit.
27. (Currently Amended) The circuit of claim 26, wherein the switched-capacitor sampling circuit includes an input, an output, and a capacitor having a first node and a second node, wherein the input of the switched-capacitor sampling circuit is coupled to the damping circuit, and wherein the first node of the capacitor is coupled to the switch and the second node of the capacitor is coupled to ~~an~~ the output of the ~~circuit~~ switched-capacitor sampling circuit.

28. (Previously Presented) The circuit of claim 18, wherein the damping circuit includes a low pass filter.
29. (Previously Presented) The circuit of claim 28, wherein the low pass filter is an RC low pass filter.
30. (Previously Presented) The circuit of claim 18, wherein the level shifting circuit is coupled between a source of the replica transistor and a drain of the source-follower transistor.
31. (Previously Presented) The circuit of claim 18, wherein the terminal of the source-follower transistor is a drain of the source-follower transistor.
32. (Previously Presented) The circuit of claim 18, wherein the level shifting circuit includes a voltage source coupled between a source of the replica transistor and the terminal of the source-follower transistor.
33. (Previously Presented) The circuit of claim 18, wherein the level shifting circuit includes a resistor and a current source, and wherein the resistor is coupled between a source of the replica transistor and the terminal of the source-follower transistor.
34. (Currently Amended) A circuit, comprising:  
a buffer including a transistor having a source and a body;

a sampling circuit capable of storing a charge of a sample, the sampling circuit including a switch, an input, and an output; and

a damping circuit coupled between the buffer and the input of the sampling circuit;

wherein the damping circuit is capable of reducing a charge glitch in response to the switch being opened or closed;

wherein the sampling circuit is a switched-capacitor sampling circuit, wherein the switched-capacitor sampling circuit includes a capacitor having a first node and a second node, and wherein the first node is coupled to the switch and the second node is coupled to the output of the sampling circuit; and

wherein the source and the body of the transistor are coupled to each other to reduce a signal dependent current.